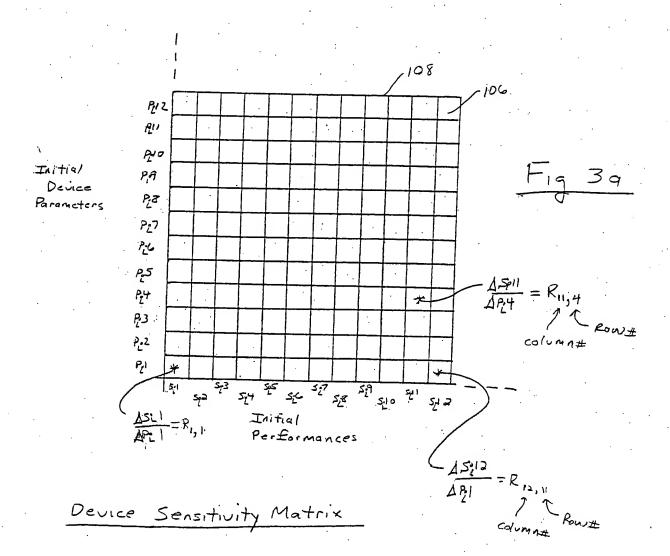
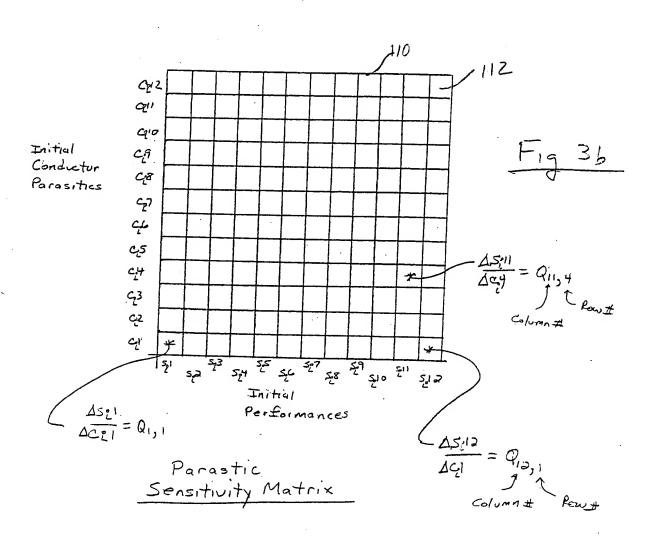


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Fiz 4

Target Performance Specification	Tolerance
丁	+x10%
Ta	+x2%
T 3	+ × 3
T 4	- ×4
T.5	+ ×5
T6	- X6%
	1

Inventors: GANG ZHANG et al.

"ANALOG INTEGRATED CIRCUIT LAYOUT DESIGN" Attorney Docket No.: 2879-030687

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